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Date

June 28, 2007

Jennifer Badley

Jennifer Badley

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Clarence T. Tegreene
Application No. : 10/813,967
Filed : March 31, 2004
TITLE : MOTE NETWORKS HAVING DIRECTIONAL
ANTENNAS
Confirmation No. : 9160

Examiner : Andrew Wendell
Art Unit : 2618
Docket No. : 0104-003-006-000000
Customer No. : 44,765

Mail Stop AF
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U.S. Patent & Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

SUBMITTED AFTER FINAL OFFICE ACTION AND

PETITION UNDER 37 C.F.R. § 1.97(d)

07/03/2007 EFLORES 00000131 10813967

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Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(d), Applicant brings to the attention of the Examiner the documents listed on the attached PTO-1449 (substitute form). This Information Disclosure Statement is being filed after a Final Office Action,

and is accompanied by the petition fee and certification as specified under § 1.97(e). Applicant respectfully petitions and requests that the Examiner consider the listed documents and evidence that consideration by making appropriate notations on the attached form. Pursuant to 37 CFR 1.98(a)(2), copies of the foreign patent documents and non-patent literature are attached.

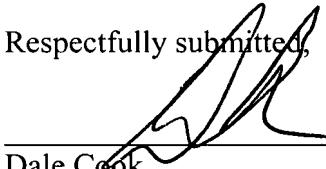
This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." Applicant reserves the right to present to the Office the relevant facts and law regarding the appropriate status of such documents.

In accordance with Section 1.97(c), the required fee is being submitted. The Director is authorized to charge any other fees which may be required, or credit any overpayment to Deposit Account No. 502857. A duplicate copy of this sheet is enclosed.

Based on reasonable inquiry, no document listed in this Information Disclosure Statement was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing date of this Information Disclosure Statement.

Applicant further reserves the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

Respectfully submitted,


Dale Cook
Registration No. 42,434

DRC:jmb

Enclosures:

Postcard
Check
Copy of this Information Disclosure Statement
Form PTO-1449 (substitute)
Cited Reference (4)

Searete LLC
1756 – 114th Ave. S.E., Suite 110
Bellevue, WA 98004
(425) 467-2300
(425) 467-2351 Facsimile

<http://isf.intven.com/shared/documents/Patents Filed-Active Drafts/Data Architecture-003/0104-003-006-Mote Networks Having Directional Antennas/IDS-after finalORallow3.doc>



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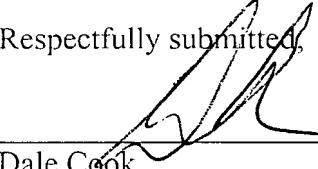
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Dale Cook
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Substitute for form 1449A/PTO

SUPPLEMENTAL INFORMATION
DISCLOSURE STATEMENT BY
APPLICANTS
(Use as many sheets as necessary)

Sheet 1 of 1 Attorney Docket Number 0104-003-006-000000

U.S. PATENT DOCUMENTS				
Examiner Initials	Cite No. ¹	U.S. Patent Number Number-Kind Code ² (if known)	Issue Date (MM-DD-YYYY)	Name of Patentee
	AA	5,581,694	12-03-1996	Iverson et al.
	AB			

U.S. PATENT APPLICATION DOCUMENTS				
Examiner Initials	Cite No. ¹	U.S. Patent Application Publication Number Number-Kind Code ² (if known)	Publication Date (MM-DD-YYYY)	Name of Applicant
	AC	2006/0224434 A1	10-05-2006	Rumi et al.
	AD	2006/0212570 A1	09-21-2006	Artsuka et al.

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
	AE	CULLER, DAVID; ESTRIN, DEBORAH; SRIVASTAVA, MANI; "Overview Of Sensor Networks"; Computer; August 2004; pp. 41-49; Vol. 37; No. 8; IEEE Computer Society		
	AF	CULLER, DAVID; MULDER, HANS; "Smart Sensors to Network the World"; Intel.com; located at: http://www.intel.com/research/exploratory/smartnetworks.htm ; pp. 1-10; printed on June 26, 2007; Intel Corporation		
	AG	FALL, KEVIN; "A Delay-Tolerant Network Architecture for Challenged Internets"; Intel Research, Berkeley; Bearing a date of February 26, 2003; pp. 1-14		
	AH	GOVINDAN, RAMESH et al.; "Tenet: An Architecture for Tiered Embedded Networks"; pp. 1-8		
	AI	STATHOPOULOS, THANOS et al.; "Mote Herding for Tiered Wireless Sensor Networks"; Center for Embedded Networked Sensing; pp. 1-14		

OMB 0651-0031

Examiner Signature	Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to indicate here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS.

SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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